


Embedded Systems - An Application-Driven Approach

Ideathon-Evaluation Team Details

Name	Profile
 <p>Prof. Rajat Moona Director Indian Institute of Technology, Bhilai</p>	<p>Prof. Moona heads the Indian Institute of Technology, Bhilai (IIT Bhilai), which is one of the six new IITs established by the Ministry of Human Resource Development, Government of India. Prof. Moona is also a Professor of Computer Science at IIT Kanpur and has supervised several postgraduate theses. He along with his students and colleagues, has authored 10 patents, several research papers and books. He had been instrumental in defining applications such as smart card driving license, vehicle registration, e-passport, electronic toll collection, mobility card, etc. The research area of Prof. Rajat Moona spans over embedded computing, computer security, VLSI design and Operating Systems. In the past Prof. Moona was the Director of the Centre for Development of Advanced Computing (C-DAC) in capacity of Director General. Prof. Moona is recipient of several awards and recognitions, including Indo-US Science and Technology Fellowship, Poonam and Prabhu Goel Chair Professorship, VASVIK Award for the year 2010, IESA Techno-visionary Award 2014, fellowship of Maharashtra Academy of Sciences 2015 and National award of Election Commission of India 2016 for the best electoral practices. Prof. Rajat Moona is an active member of the technical committee of Election Commission of India for EVMs and has been instrumental in defining the EVMs and VVPAT. He has also been instrumental in defining national voter service portal and national electoral search for the voters.</p>



Satheesh Balasubramanian
Distinguished Engineer
Arm, India

Satheesh Balasubramanian is a Distinguished Engineer in the Physical Design Group (PDG) of Arm. He is based in Bangalore. Satheesh works on Signoff methodologies. He has been recently involved in developing methodologies for Aging, High Sigma signoff, designing for variability and low voltage signoff.



Suraj Balaji Kamat
Senior Principal Engineer & DV Architect
Arm, India

Suraj Kamat is a Senior Principal Engineer & DV Architect in CPU Group for Arm. He is based in Bangalore. Suraj works on design and development of CPU verification methodologies and on enabling Machine learning for verification. His interests are cpu verification, data analytics and machine learning applications.



Vishal Goyal

Group Technical Marketing Manager
ASEAN, Australia, New Zealand, and India
STMicroelectronics

Vishal Goyal is a Group Technical Marketing Manager for STMicroelectronics' RF and Analog products in ASEAN, Australia, New Zealand, and India, and has held this position since 2015. Goyal started his career at STMicroelectronics India in 2002 as a Design Engineer in Central R&D and Development. In 2007, he joined the Marketing department and was responsible for Analog products for India. Goyal was appointed Senior Marketing Manager of Analog, MEMS, and Sensors for India market in 2013, and his mandate was expanded to include ASEAN, Australia, and New Zealand in 2015. Goyal actively supports customers on the full IoT product-development lifecycle and helps develop the ST ecosystem in collaboration with customers, ODMs, design houses, and distributors.

Goyal has published more than 30 technical articles around the world.

Vishal Goyal was born in India in 1979 and graduated from Indian Institute of Technology Roorkee, India and MBA from Indian Institute of Management Lucknow, India.



Varun Bansal

Business Head – India Regions (North & South)
STMicroelectronics

Varun Bansal, presently heading Business development for India regions in STMicroelectronics India, having vast experience in Semiconductor Sales & Marketing, Business Development & Application Support, Design & Development. A proven track record of developing, growing and managing successfully new business verticals & negotiating large deals, contracts/agreements. Have taken on various Leadership roles in STMicroelectronics, including Head - Automotive BU and Industrial Segment, India & SAARC.



Raunaque Mujeeb Quaiser

Group Manager, System Research and Applications
STMicroelectronics, India

Raunaque is currently working with STMicroelectronics and holds the position of Group Manager. He has total experience of 18 years in Semiconductor Industry. He has done B. Tech (Computer Engineering) from AMU and MBA from IIM Lucknow. His key responsibilities include Electronic System Development on different technologies for IoT, Smart Cities, and Smart Home. He is managing and coordinating the Startup Incubation Centre at STMicroelectronics, India. His areas of interests are Connectivity Technologies, Mobile Applications and System Development and System Integration.